

ACKNOWLEDGEMENT RECEIPT

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Title of Invention	METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES																																																																																																														
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/710616 </p> <p>EFS ID: 65188</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 30%;">Confirmation Code</th> <th style="width: 70%;">Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>4615</td> </tr> <tr> <td>USPTOEFSNot</td> <td>For assistance with e-filing a patent application, contact the Patent Electronic Business Center: Toll-Free Number:1(866) 217-9197 Website: http://www.uspto.gov/ebc/</td> </tr> </tbody> </table> <p>First Named Applicant: Jung-Ching CHEN</p> <p>Attorney Docket Number: NAUP0575USA</p> <p>Timestamp: 2004-07-25 21:59:53 EDT</p> <p>From: US</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 30%;">Doc. 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